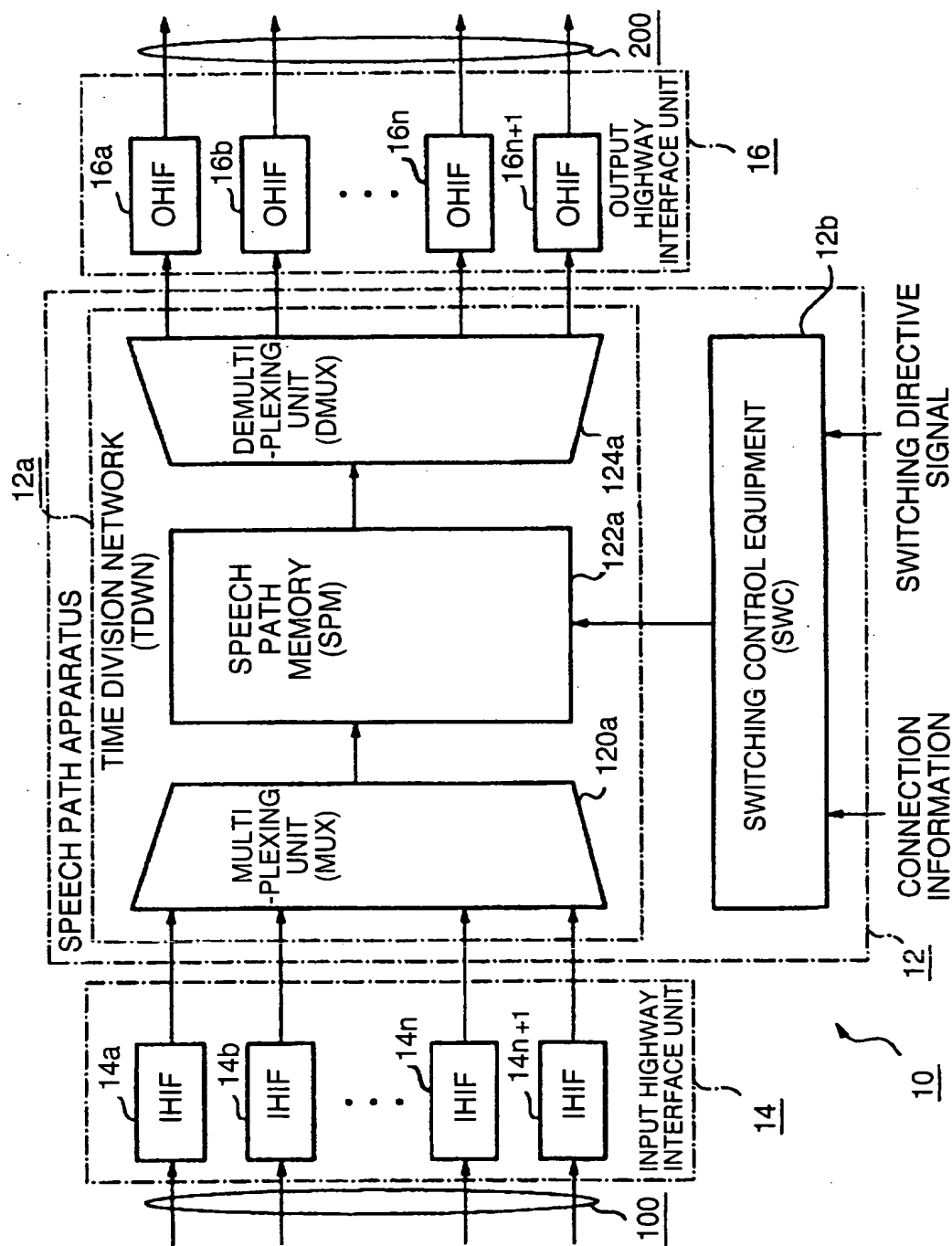
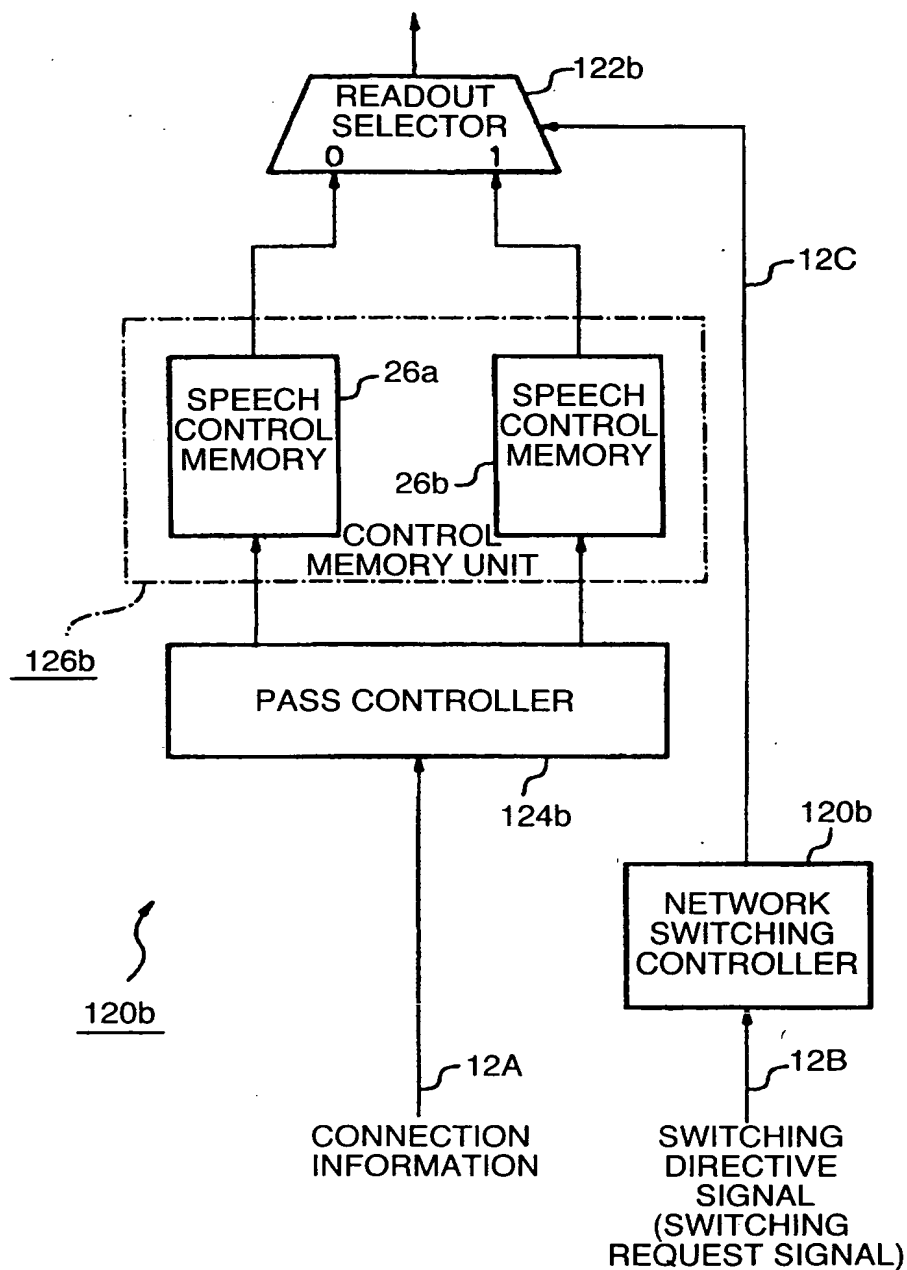


FIG. 1



SCHEMATIC CONFIGURATION OF  
DIGITAL SWITCH

FIG. 2



SCHEMATIC CONFIGURATION OF SWITCHING CONTROL EQUIPMENT



FIG. 4

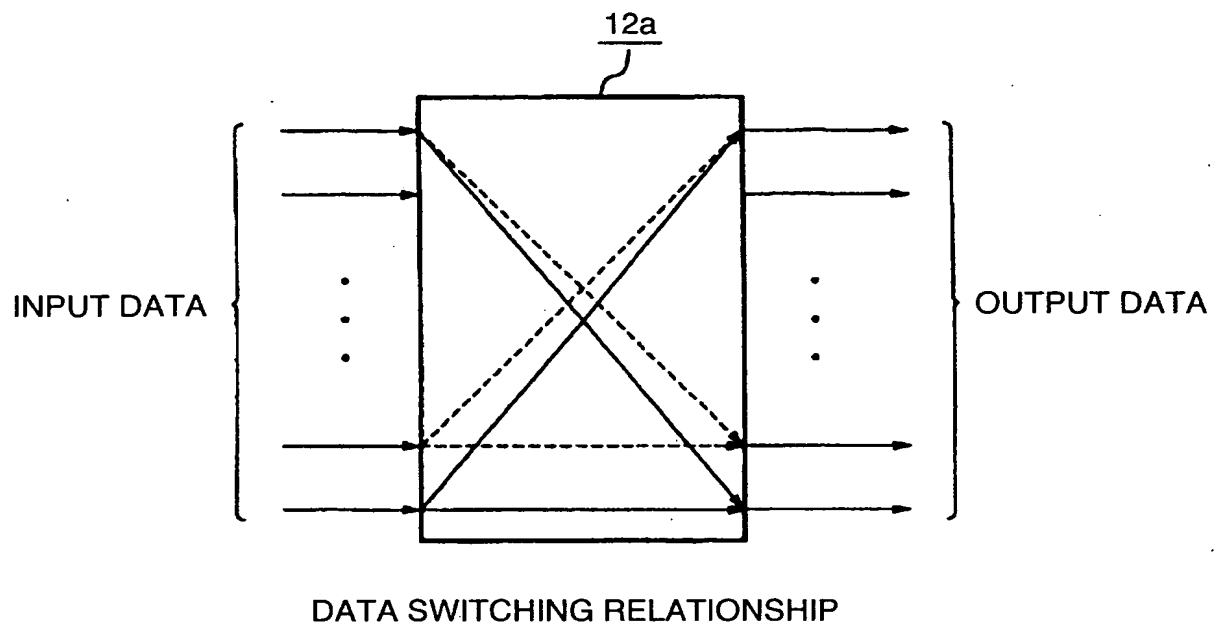
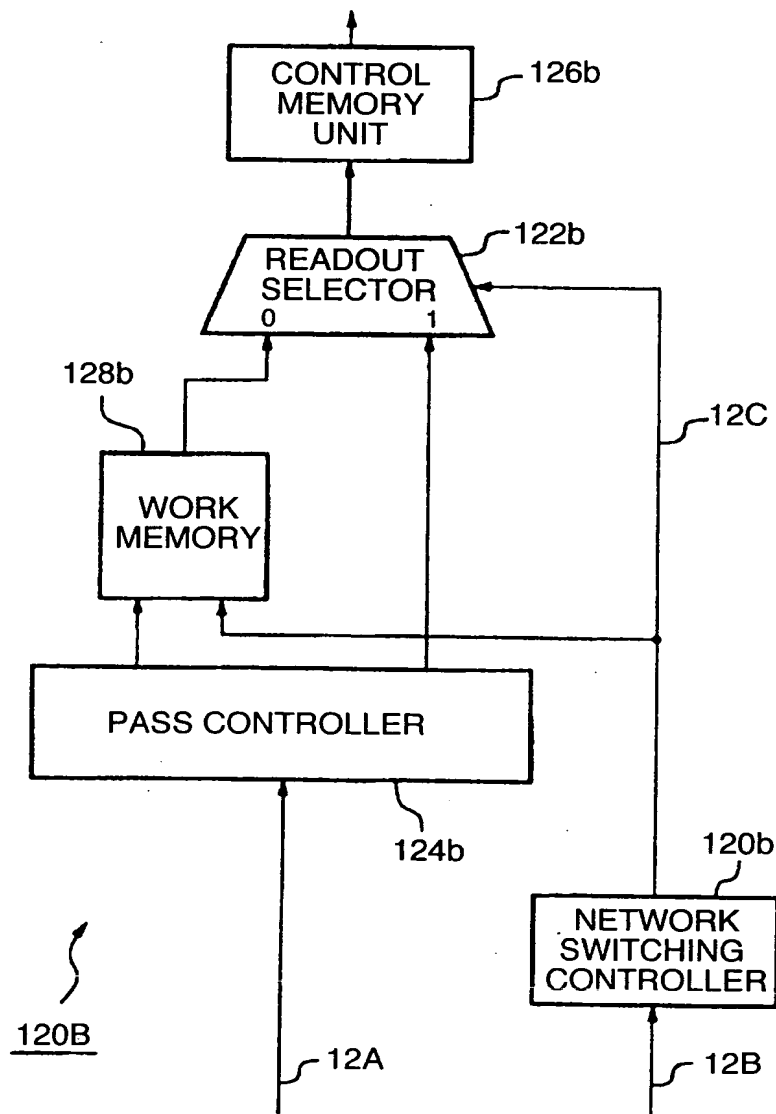




FIG. 6



CONFIGURATION OF ANOTHER EMBODIMENT  
OF SWITCHING CONTROL EQUIPMENT

FIG. 7

WORK MEMORY		
ADDRESS	WRITE ADDRESS	WRITE DATA
0	0	0
1	2	1
2	X	2
3	K-1	3
⋮		
Y-1		

CONTENTS OF MEMORY





